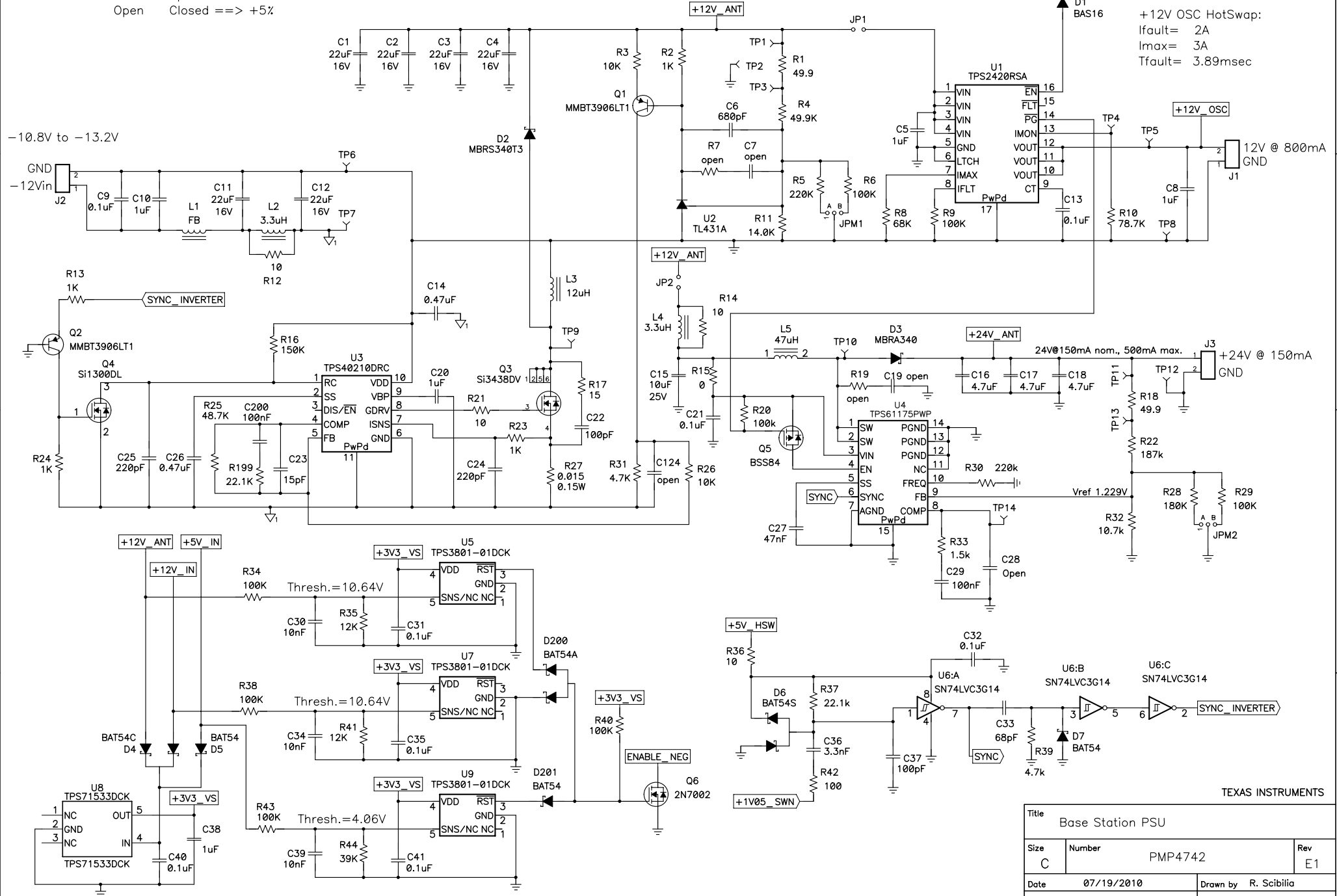


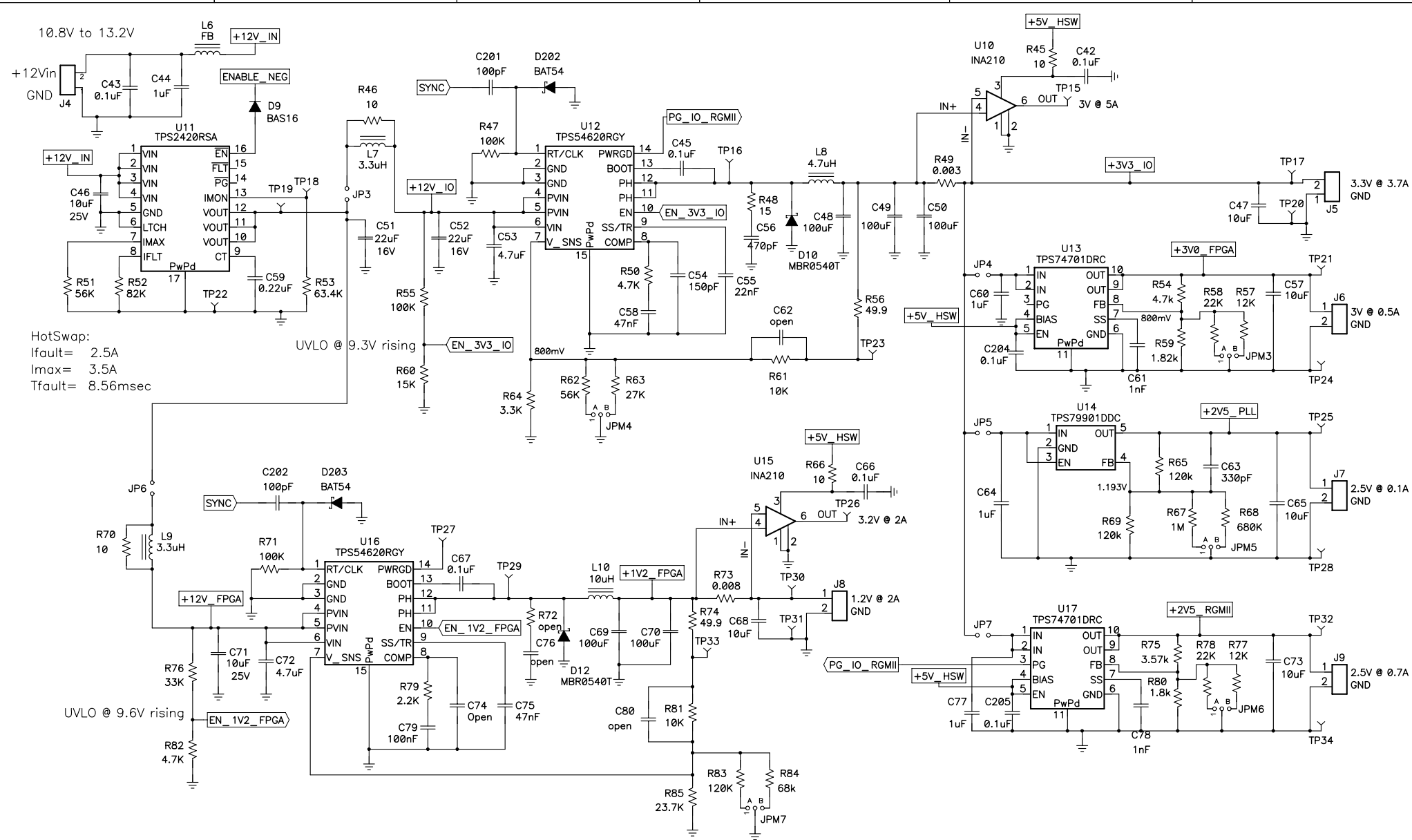
MARGINING:

JPMxx jumper: A B

Open Open ==> -5%  
 Closed Open ==> Nominal Value  
 Open Closed ==> +5%



Title		
Base Station PSU		
Size	Number	Rev
C	PMP4742	E1
Date	Drawn by	
07/19/2010	R. Scibilia	
Filename	Sheet	of
PMP4742 Rev_E1.SCH	1	3



Notes:  
 Ref. des. >= 199 were added on the board  
 Modifications from Rev.D to Rev.E1:

**SHEET1:**  
 - R13, R24 = 1K, R27 = 0.015 Ohm  
 - R7, C7 = Open, C6 = 680pF  
 - R31 = 4.7K, R26 = 10K, R20 = 100K  
 - R42 = 100 Ohm, C33 = 68pF  
 - Inserted Schottky diode on RST lines of U5, U7, U9  
 - Add R199, C200, C24=220pF, R17=15 Ohm  
 - C22=100pF, R33=1.5K, C29=100nF

**SHEET2:**  
 - C62=open, C54=150pF, C56=470pF  
 - R72=open, C76=open, R79=2.2K, C79=100nF  
 - C80=open, R48=15 Ohm  
 - Bias voltage on U13 and U17 from +5V\_HSW  
 - Inserted 100pF + Schottky diode on RT/CLK pin of U12 and U16 and removed D8 and D11  
 - Increased current on 2V5 RGMII output to 0.7A

#### SHEET3:

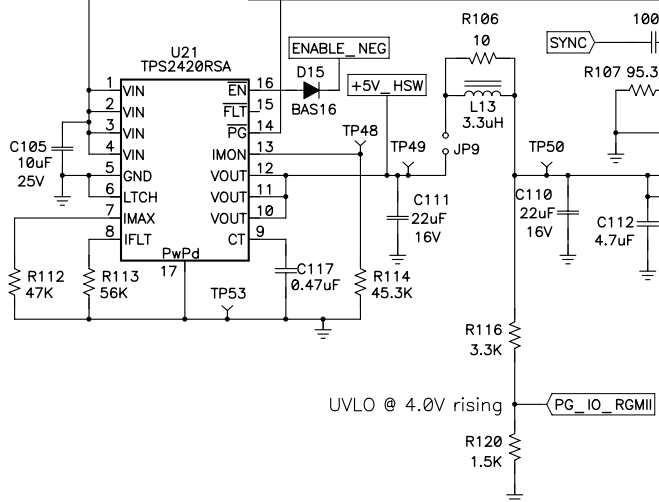
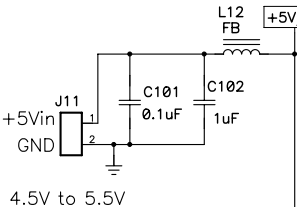
- R97 = 69.8K, R98=8.2K, C98=10nF, C99=100pF  
 - R116=3.3K, R120=1.5K, R111=4.7K, R108=15 Ohm  
 - C115=470pF, C118=open, C97=47uF  
 - Bias voltage on U13 and U17 from +5V\_HSW  
 - Inserted 100pF + Schottky diode on RT/CLK pin of U22 and removed D14  
 - Inserted a pullup resistor on Enable pin of U23 for test purpose  
 - Disconnected TP43 from 1V05 and connected it to TP38  
 - Disconnected R110 from C109 and connected it to TP47

TEXAS INSTRUMENTS

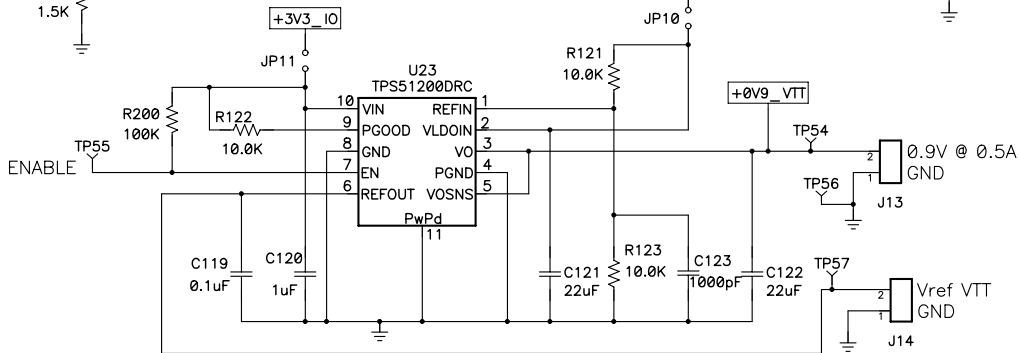
Title Base Station PSU		
Size C	Number PMP4742	Rev E1
Date 07/19/2010	Drawn by R. Scibilia	
Filename PMP4742 Rev_E1.SCH	Sheet 2	of 3

NOTES:  
=====  
1) Overcurrent set @ 9.1A  
2) U19 synchronizes all the other converters

HotSwap:  
Ifault= 3.5A  
Imax= 4.5A  
Tfault= 26.45msec



UVLO @ 4.0V rising



Title Base Station PSU			
Size C	Number PMP4742	Rev E1	
Date 07/19/2010	Drawn by R. Scibilia		
Filename PMP4742 Rev_E1.SCH	Sheet 3	of 3	

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