

Application Report SLVA424-April 2010

# Importing a SPICE NetList into TINA-TI

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### ABSTRACT

This application note describes the procedure for importing an unencrypted SPICE netlist into <u>TINA-TI</u> simulation software, creating a new macromodel based on the netlist, and placing a symbol to instantiate the macromodel in a TINA-TI circuit schematic. The behavior of the macromodel depends on the compatibility of the netlist entries with the TINA-TI software. It is possible that some SPICE-based netlists may contain formats or executable statements that are not compatible with TINA<sup>TM</sup>. If the SPICE netlist is encrypted and cannot be imported directly into TINA, contact the TI E2E/Simulation and Models Forum for support (www.ti.com/e2e-simulation).

The first section gives an example of the step-by-step procedure for importing a SPICE netlist to create a TINA-TI macromodel. Then we illustrate how to create an instance of the TINA macromodel on a circuit schematic. This report uses the example of a PSPICE netlist for an <u>OPA830</u>, a low-power, high-speed operational amplifier with rail-to-rail output.

**NOTE:** This document does not address the details and best practices of generating SPICE subcircuits. For more information on this topic, see Reference (1).

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Procedure

## 1 Procedure

Follow this procedure to import a SPICE netlist in order to create a TINA-TI macromodel.

## 1. Importing a SPICE netlist file.

The netlist in question must be configured using a SPICE subcircuit statement, and the netlist file must have a \*.*cir* extension. If the netlist file has a different extension (such as \*.*sub*), change the extension to \*.*cir* before trying to import it into TINA-TI. Here is an example from the OPA830 PSpice netlist: .SUBCKT OPAx83x\_Model + - Dis OUT V+ V-

# 2. Confirming that the SPICE netlist will compile in TINA.

Some SPICE netlists may contain statements and/or formatting that is incompatible with TINA. To check for this potential incompatibility, open the TINA program and select the *File/Import/Pspice Netlist* (\*.*CIR*) menu tab as shown in Figure 1.

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Figure 1. Menu Tabs for Importing a PSpice Netlist as a \*.CIR File



Navigate to the desired file and select it using the open directory window. A netlist editor window opens, as shown in Figure 2. Select the check-box icon on the upper menu bar as indicated by the black arrow in Figure 2. If the netlist format and executable statements are compatible with TINA, then a message that says *Successfully completed* appears in the lower margin of the window.

🛃 <opax83x model.cir=""> - Netlist Editor</opax83x>	
File Edit Analysis Help	
*OPA830 Low-Power, Single-Supply, Wideband Operational Amplifier	
* REV. A - Created 7/03/06 X-ramus2@ti.com	
*	
*	
* NOTES:	
* 1- This macromodel predicts well: DC, small-signal AC, noise,	
* , and transient performance under a wide range	
* of conditions.	
* 2- This macromodel does not predict well: distortion	
* (harmonic, intermod, diff. gain & phase,),	
* temperature effects, board parasitics, differences	
* between package styles, and process changes	
3- For Spice3F4 users they might need to un-comment the lines for the F	
Tunction and comment out the Lines for Pspice F functions	
and subckts. First try the present hetlist then comment out lines if	
- errors appear.	
- General Loim; + BYYYYYYY MI NE ZDOLY/MD)N UNI ZUM2 N DO ZDI N ZTC= N	
* Franciae	
* F1 12 10 VCC 1Ma 1 3M	
* 4- For some simulators the subckt for the E statement need to be placed	
* inside the ends statement followed by carriage return	
* 5- Known Problems: - Output current capability on +5V too low	
* - Most positive output voltage on +3V too low	
*	
*	
*   This macro model is being supplied as an aid to	
*   circuit designs. While it reflects reasonably close	_
ad	<b>_</b>
Line: 1 Col: 1 Successfully compiled.	

Figure 2. Netlist Editor Window Used to Check PSpice Netlist Compatibility with TINA-TI

## 3. Creating the TINA-TI macromodel.

Open TINA-TI and select the *Tools/New Macro Wizard* menu tab. A new window should appear, as shown in Figure 3.

New Macro Wizard		×
<u>N</u> ame:		
		Empty circuit
C <u>o</u> ntent:	··· ·· ··	🗖 Current circuit
<u>S</u> hape:	<auto></auto>	Auto-generated
Defaults		Vhdl
<u>L</u> abel:	SCK#	☐ <u>G</u> enerate VHDL component
Parameters:		
	🖉 OK 🗙 Cance	el <b>?</b> <u>H</u> elp





Procedure

Enter a name in the *Name* field. Be sure that the *Current circuit* box is unchecked, then click the ellipsis to the right of the *Content* field. A directory window then opens. Navigate to the folder containing the SPICE netlist file and click on the file icon. In our example, the *New Macro Wizard* window should appear as Figure 4 illustrates.

New Macro Wizard	×
<u>N</u> ame:	0PA830
C <u>o</u> ntent:	OPAx83x Model.cir Empty circuit
<u>S</u> hape:	<auto></auto>
Defaults	SCK# Generate VHDL component
Parameters:	
	V OK X Cancel ? Help

Figure 4. New Macro Wizard Window with Entries

Leave the check-mark in the *Auto-generated* box and click the *OK* button. A directory window opens with a default path to the TINA-TI **MACROLIB** folder as shown in Figure 5.

Save Schematic							<u>?</u> ×
Save in	: 🗀 Tina 7 - TI			•	( <del>-</del>	💣 🎟 •	
My Recent Documents Desktop My Documents	BUTTONS cache EDS.PCB EXAMPLES MACROLIB ORCAD.PCB PCAD.PCB PROTEL.PCB REDAC.PCB SPICELIB TANGO.PCB TINA.PCB						
🧐 My Network	File name: Save as type:	OPAx83x M Schematic	Model.TSM :s Macro (*.TS	M)		•	Save Cancel
Places							Help

# Figure 5. Initial Window for Saving Instantiated Macromodel

Navigate to the desired directory folder and click the *Save* button. This directory is the destination of the instantiated TINA-TI macromodel based on the imported SPICE subcircuit netlist.



# 4. Placing an instance of the new macromodel in a TINA-TI circuit schematic.

After the new macromodel file (\*.TSM) has been created and saved, the macromodel may be instantiated (placed) into a circuit schematic by first selecting the *Insert/Macro* menu tab and navigating the open window to the directory that contains the macromodel file. Select the desired macromodel file (in our example, *OPAx83x.TSM*) and select the *Open* button. The window closes, and the mouse cursor then shows the outline of the autogenerated shape. Click the mouse when the cursor is in the desired position in the schematic window; the shape should appear as shown in Figure 6.

🛐 Noname - Schematic Editor		
File Edit Insert View Analysis T&M Tools Help		
	🔊 Ground	•
		Þ
Basic Switches (Meters (Sources (Semiconductors (Spice Macros)		
UI OPA830 = + OPA830 + OPA830 + DIS V+ + = +		
<u>۱</u>		
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Figure 6. Newly Created Macromodel Placed in a Schematic Window

Complete the circuit by inserting and connecting additional components, and run the desired simulations. An example of a final schematic and the probe window that shows the output waveform from a transient simulation appear in Figure 7 and Figure 8, respectively.



Figure 7. Final Schematic Showing Macromodel Shape and Additional Components





# 5. Viewing the netlist.

If the netlist is unencrypted, it is possible to view the macromodel netlist in TINA. To view the netlist, click the macromodel symbol and select *Enter Macro* in the pop-up menu The netlist viewer window then opens within the TINA application window as shown in Figure 9. The schematic can still be viewed by selecting the appropriate tab on the lower left corner of the TINA application window. To close the netlist viewer, select the *File/Close* menu tab. (Note the tabs for the schematic window and the viewer window [lower left].)

Figure 9. Netlist Viewer Window

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Procedure



References

www.ti.com

If the netlist is encrypted, it is not possible to view the netlist content from within TINA or any other text viewer. As an example, Figure 10 shows the netlist view of the TPS6300 macromodel. If the SPICE netlist is encrypted and cannot be imported directly into TINA-TI software, contact the E2E/Simulation and Models Forum for support (www.ti.com/e2e-simulation).

🕺 <noname:u1 [macro]=""> - Netlist Viewer</noname:u1>	<u> </u>
<u>Eile Edit Analysis H</u> elp	
*TPS63000	-
* (C) Copyright 2009 Texas Instruments Incorporated. All rights reserved.	
** This model is designed as an aid for customers of Texas Instruments.	
** TI and its licensors and suppliers make no warranties, either expressed	
** or implied, with respect to this model, including the warranties of	
** merchantability or fitness for a particular purpose. The model is	
** provided solely on an "as is" basis. The entire risk as to its quality	
**************************************	
*	
** Released by: Analog eLab Design Center, Texas Instruments Inc.	
* Part: TPS63000	
* Date: 03/29/2010	
* Simulator: TINA	
* Simulator Version: 7.0.80.96 SF-TI	
* EVM Order Number: TPS63000EVM-148	
* EVM Users Guide: SLVU156 - March 2006	
* Datasheet: SLVS52UB - March 2006, Revised July 2008	
* Model Version: Final 1.00	
*****	
*	
* Updates:	
* Finel 1 00	
* Release to Web.	
*	
* * * * * * * * * * * * * * * * * * * *	
* Encrypted macro. Content can't be viewed.	
व	
Line: 1 Col: 1	
Noname U1	

Figure 10. Netlist Editor Display for an Encrypted Netlist

# 2 References

8

1. Vladimirescu, V. (1994). The Spice Book. John Wiley and Sons, Inc. New York. ISBN 0-471-60926-9

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Interface	interface.ti.com	Energy	www.ti.com/energy
Logic	logic.ti.com	Industrial	www.ti.com/industrial
Power Mgmt	power.ti.com	Medical	www.ti.com/medical
Microcontrollers	microcontroller.ti.com	Security	www.ti.com/security
RFID	www.ti-rfid.com	Space, Avionics & Defense	www.ti.com/space-avionics-defense
RF/IF and ZigBee® Solutions	www.ti.com/lprf	Video and Imaging	www.ti.com/video
		Wireless	www.ti.com/wireless-apps

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