1. 根据 CCSv5 按照使用指南安装 CCS 开发环境,选择免费版本的权限,可直接使用 XDS100 仿真器。

2. 安装 ControlSUITE 软件,该软件 TI C2000 的一个资源包,包括所有的器件的数据手册、 使用指南、示例代码、开发工具软硬件、C2000 函数库等,非常方便。

F2837xD 的例程在路径下: C:\ti\controlSUITE\device\_support\F2837xD\v110

F2837xD 开发板资料:

 $C:\ti\controlSUITE\development\kits\controlCARDs\TMDSCNCD28377D\v1\_1\R1\_1$ 

3. 安装完成后,双击打开 CCSv5,选择一个合适的工作区,默认亦可。



4.进入 CCS 编辑界面,展开左侧的项目管理栏,TI Resource Explorer 可选关闭。



8	C	CS Edit -	Code	Comp	oser	Stud	io			0	١.,										- 0	x
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## 5. 导入 F2837xD 例程:

C:\ti\controlSUITE\device\_support\F2837xD\v110\F2837xD\_examples\_Cpu1

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File Edit View Navigate	Proj	ect Run Scripts	Window Help					
📑 🕶 🗟 🖷 🚿 👻	<b>P</b>	New CCS Project					📑 📴 CCS Ed	fit
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	010	Build All	Ctrl+B					
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n Scripts Window Help	Select root directory of the projects to import
Timport CCS Eclipse Projects	
Select Existing CCS Eclipse Project Select a directory to search for existing CCS Eclipse projects.	▶              ↓             £2805x             ↓             ↓
Select sgarch-directory:     Browse     Select archive file:     Discovered projects:	<ul> <li>▶ ↓ f2837x_internal</li> <li>₽ ↓ f2837x_D</li> <li>▶ ↓ 100</li> <li>↓ ↓ 110</li> </ul>
Select All Deselect All Refresh Copy projects into workspace Automatically import referenced projects Open the Resource Explorer and browse available example projects	doc         P → F2837xD_common         F2837xD_examples_Cpu1         P → adc_ppb_delay         P → adc_ppb_delay         P → adc_ppb_offset         P → adc_soc_continuous         P → adc_soc_continuous         P → adc_soc_software
(?) Einish Cancel	b linky       b linky_with_DCSM       Eolder:       Make New Folder       OK

## 如选 ADC 的例程:

Browse For Folder	×	Import CCS Eclipse Projects
Select root directory of the projects to import		Select Existing CCS Eclipse Project Select a directory to search for existing CCS Eclipse projects.
<ul> <li>V100</li> <li>V110</li> <li>doc</li> <li>F2837xD_common</li> <li>F2837xD_examples_Cpu1</li> <li>adc_ppb_delay</li> <li>adc_ppb_limits</li> <li>adc_ppb_offset</li> </ul>	E	Select search-directory: C:\ti\controlSUITE\device_suppor Browse   Select archive file:   Discovered projects:     Image: C:\ti\controlSUITE\d   Select All
<ul> <li>adc_soc_continuous</li> <li>adc_soc_epwm</li> <li>adc_soc_software</li> <li>epu01</li> <li>blinky</li> <li>blinky_with_DCSM</li> <li>buffdac_enable</li> <li>can_loopback</li> <li>can_loopback interrupts</li> </ul>	Ŧ	
Eolder: adc_soc_software           Make New Folder         OK         Car	ncel	? <u>Einish</u> Cancel

6. 查看工程: 当导入工程后,可以看到工程出现在 Project Explorer 中,单击选择该工程名称(使其 Active 状态,当 Project Explorer 中有多个工程的时候,通过单击某工程名称,即

可使其为当前 active 的工程),展开该工程,即可看到许多源文件,双击源文件的名称,即可在编辑主界面看到该源文件的代码,如下图:

CCS Edit - adc_soc_software_cpu01/adc_soc_software	_cpu01.c - Code Com	nposer Studio					_ <b>D</b> _ X
<u>File Edit View Navigate Project Run Scripts</u>	<u>W</u> indow <u>H</u> elp						
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🎦 Project Explorer 🛛 📃 🚖 🍸 🗖 🗖	adc_soc_softwar	re_cpu01.c 🖂					
<ul> <li>■ Grade Cosc Software c.pu01 (Active - CPU1 RAM)</li> <li>■ Includes</li> <li>■ Gra37xD_CodeStanBanch.asm</li> <li>■ Fra37xD_CodeStanBanch.asm</li> <li>■ Fra37xD_GradeStanBanch.asm</li> <li>■ Fra37xD_GradeStanBanch.asm</li> <li>■ Fra37xD_GradeStanBanch.asm</li> <li>■ Fra37xD_GradeStanBanch.asm</li> <li>■ Fra37xD_GradeStanBanch.asm</li> <li>■ Fra37xD_GradeStanBanch.asm</li> <li>■ Fra37xD_FradeStanBanch.asm</li> <li>■ Fra37xD_FradeStanBanch.asm</li> <li>■ Fra37xD_FradeStanBanch.asm</li> <li>■ Fra37xD_FradeStanBanch.asm</li> <li>■ Fra37xD_FradeStanBanch.asm</li> <li>■ Fra37xD_FradeStanBanch.asm</li> <li>■ FradeStanD_FradeStanBanch.asm</li> <li>■ FradeStanBanch.asm</li> <li>■ FradeS</li></ul>	C ac so control 1//iminume 2//FILE: 3// TITLE: 4// 5//1 \addto 6//1 \ch2 A 7//1 8//1 This e 9//1 tris e 12//1 \b Add 15//1 \b Add 15//1 \b Add 16//1 \b Add 17//1 18// 20// \$TI Rel 21// \$Releas 22//iminume 23 24 #include ** 25 0 tems Description	recount commentations and commentation a	<pre>####################################</pre>	F 2837x0. S on ADCA as y will contain the sentation of sentation consentation consenta	and ADCB based in: if the voltage of if the voltage of if the voltage of the voltage of the voltage of the voltage of <b>Path</b>	on a software on pin A0\n on pin A0\n on pin 80\n on pin 81\n Include File	× E Type
• • • • • • • • • • • • • • • • • • •							
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7.编译工程,点击工具栏的编译按钮 <sup>《</sup>,也可从右键工程的菜单中选择编译工程, Problem 显示编译结果。



8. 调试工程之前,要先建立目标配置文件(target configuration),即配置仿真器型号,芯片信号。

CCS Edit - adc\_soc\_software\_cpu01/adc\_soc\_software\_cpu01.c - Code Composer Studio - 0 × File Edit View Navigate Project Run Scripts Window Help TI Resource Explorer 🔚 i 🍤 🖕 🔶 🗸 😭 📴 CCS Edit 🏠 Project 🥡 GUI Composer™ adc\_soc\_software\_cpu01.c 🛛 - -> 0 😰 Target Configurations 🔀 + 🔺 📛 ado Applications 🖹 🗙 🛷 ⊟ Grace Snippets type filter text 😑 Projects 🗁 陷 Project Explorer Cutline 🛛 🔂 User Defined 7 //! O Advice 8//! This example converts some voltages on ADCA and ADCB R 🖟 🛃 Problems 9//! trigger. 10 //! 10//!
10//!
11//! After the program runs, the memory will contain:
12//!
13//! \b AdcaResult0 \b: a digital representation of the vo
14//! \b AdcaResult1 \b: a digital representation of the vo
15//! \b AdcbResult0 \b: a digital representation of the vo
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17/! \b AdcbResult1 \b: a digital representation of the vo
17/! \b AdcbResult1 \b: a digital representation of the Console 🗟 🏇 Debug Memory Browser 17 //! 18 // 1010 Registers 60 Expressions (x)= Variables E Disassembly • Breakpoints Click the New button to create a new Modules target configuration file. Click <u>here</u> to hide this message. Other.. Console 🖄 🖓 Advice Resou -l"libc.a <Linking> 'Finished building target: adc\_soc\_software\_cpu01.out \*\*\*\* Build Finished \*\*\*\* ļ • Licensed 

通过 View>target configuration 调出目标配置文件的管理栏,如下图右侧:

建立新 F2837x 的目标配置文件:点击新建按钮 Sile name 中输入名称,如 F2837x\_XDS100.ccxml

	🔛 🕞 CCS Edit	
New Target Configuration	Target Configurations 🛛	
Target Configuration     type	De filter text	<mark>ጶ</mark>
File name NewTargetConfiguration.ccxm	Be User Defined	
✓ Use shared location		
Location: C:/Users/A0220213/ti/CCSTargetConfigurations File System		
Einish         Cancel         Clic           Image: Control of the second s	ck the New button to create a r get configuration file. Click <u>her</u> le this message.	e to
Console 🛛 🗌 🖸 🖓 Advis	ce 🗸	

## 弹出来的新建目标配置文件对话框:

在 connection 中选择 XDS100v2 的仿真器(开发板自带), Board or Device 中选择 F2837x 的芯片, 然后点击右侧的 Save 按钮。最后即可关闭它 译 F2837x\_XDS100.ccxml ②。



这样一个新的目标配置文件即建立成功,然后在右侧目标配置管理栏中设置它为默认。



9. 调试工程: 连接 F2837xD 的开发板到电脑 USB 口(底座 USB 供电,控制卡上的 USB 为 仿真器接口),上电。



按照步骤 8 设置 F2837x\_XDS100.ccxml 目标配置为默认。在 Project Explorer 中选择要调试的工程,点击调试按钮 \*\*\*,只选择 CPU1,进入调试界面。CCS 会通过 F2837x\_XDS100.ccxml 的配置自动连接芯片,加载例程的.out 文件到芯片中。

CCS Edit - adc_soc_software_cpu01/ad	c_soc_softwa <mark>re_cpu01.c - Code Co</mark> mposer Studio	
<u>File Edit View Navigate Project</u>	<u>R</u> un Scripts <u>W</u> indow <u>H</u> elp	
📑 • 🖬 💿 🚳 • 🏇 🕖		😭 📴 CCS Edit
Project Explorer 🔀 📃 🗖	adc_soc_software_cpu01.c 🛛 🗖 🗖	🖹 Target Configurations 🛛 📃 🗖
	10//!	😨 🗶 😓
adc_soc_software_cpu01 [Activ	e - CPU1_RAM]	hme filter text
Binaries	13//! \b AdcaResult0 \b: a digital representation of the voltage on pin AG	
Includes	14//! \b AddaResult1 \b: a digital representation of the voltage on pin Al	28035.ccxmi
CPU1_RAM	16//! \b AdcbResult1 \b: a digital representation of the voltage on pin B1	28035_xds510.ccxmi
Adc_soc_software_cpu01.c	17 //!	320053.ccxmi
F2837xD_CodeStartBranch.asr	18 //	320002.CCXmi
F2837xD_DefaultISR.c	19//###################################	20009.cccmi
F2837xD_GlobalVariableDefs.c	20// \$11 Release Pate: Mon Apr. 7 10:35:37 CDT 2014 \$	2009_xus510.ccxmi
F2837xD_Gpio.c	22 //**********************************	2000.CCATH
▷ R F2837xD_Ipc.c	23	200000 200000 20000 20000 20000 20000 20000 20000 20000 20000 20000 20000 200000 20000 20000 20000 20000 20000 20000 20000 20000 20000 20000 20000 20000 20000 20000 20000 20000 2000000
F2837xD_PieCtrl.c	24 #include "F28x_Project.h" // Device <u>Headerfile</u> and Examples Include	2837x xds510 ccyml
▷ L F2837xD_PieVect.c	25 26 //definitions for selecting ADC resolution	28m35 ccvml
▷ L F2837xD_SysCtrl.c	27 #define RESOLUTION 12BIT 0 //12-bit resolution	28m25serial cryml
▷ Land F2837xD_usDelay.asm	28 #define RESOLUTION_16BIT 1 //16-bit resolution (not supported for all va	F2837x XDS100 ccxml [Default]
	29//definitions for selecting ADC signal mode	R NewTargetConfiguration ( cml
	30 #define SIGNAL_SINGLE 0 //single-ended channel conversions (12-bit mode	
	32	guad motor control.ccxml
	33 void ConfigureADC(void);	
	34 void SetupADCSoftware(void);	
	35	Click the New button to create a new target
	36 //variables to store conversion results	configuration file. Click here to hide this
		message.
	📮 Console 🛛 🧼 🔂 🚱 📑 🚮 🜬 📑 🖬 📮 🕶 🗖 🔛 Problems 🖄 💡 Advis	ce 🗸 🖓 🖓
	CDT Build Console [adc_soc_software_cpu01] 0 items	
	"./F2837xD_DefaultISR.obj"   Description	Resource Pati
	"./F2837xD_CodeStartBranch.obj" -1"rts2800_fpu32.1ib"	
	-1 2057X_RAM_INK_CPUI.cmd	
	<pre><li><linking></linking></li></pre>	
	'Finished building target: adc_soc_software_cpu01.out'	
	**** Build Finished ****	
4 m +	* * *	Þ
📑 Licensed	adc_soc_software_cpu01	

Caunching Debug Session	
The project adc_soc_software_cpu01 is compatible with multiple CPUs in the ta	arget configuration.
Please select the CPUs to load the program on:	
V Texas Instruments XDS100v2 USB Emulator_0/C28xx_CPU1	
Texas Instruments XDS100v2 USB Emulator_0/C28xx_CPU2	
<u>S</u> elect All	Deselect All
Create a debug group for selected cores	
Make the group synchronous	
ОК	Cancel

10. 进入调试界面进行调试(调试界面和编辑界面可在右上角进行切换

🖹 🏷 CCS Debug 🖫 CCS Edit 🛛						
CCS Debug - adc_soc_software_cpu01/adc_soc_software_cpu01.c - Code Composer Studio	Harrison	In the late		-	-	- 0 X
File Edit View Project Tools Run Scripts Window Help						
📫 🕶 🔚 🐘 🗱 🕸 🕶 🗐 🌮 🖉 🗫					🖹 🎭 CCS De	ebug) 🖶 ( 🐃
🌾 Debug 🛛 🥂 🦓 🕪 💷 🔳 🔍 🖘 🐟 🖈 🍐 🍫 👻 🖾	(×)= Variables	🕸 Expressions 🛛	1919 Registers			- 8
adc_soc_software_cpu01 [Code Composer Studio - Device Debugging]				🏝 🏘 📄	🕂 🗙 🔆 🖗	📫 🛃 🏟 🎽
Texas Instruments XDS100v2 USB Emulator_0/C28xx_CPU1 (Suspended - SW Breakpc	Expression	Туре	2	Value		Address
args main() at acc_soc_software_cpubl.c:48 0x008000	🛉 Add i	new expressior				
<pre>c_int00() at boot28.inc:223 0x00810F (the entry point was reached)</pre>						
🖉 Texas Instruments XDS100v2 USB Emulator_0/CPU1_CLA1 (Disconnected : Unknown						
Texas Instruments XDS100v2 USB Emulator_0/C28xx_CPU2 (Disconnected : Unknowr Texas Instruments XDS100v2 USB Emulator_0/C28xx_CPU2 (Disconnected : Unknowr						
Yexas Instruments XDS100v2 USB Emulator_0/CPU2_CLA1 (Disconnected : Unknown *						•
adc_soc_software_cpublic &						
<pre>43 { 44 45// Step 1. Initialize System Control: 45// PLL, WatchDog, enable Peripheral Clocks 47// This example function is found in the F2837xD_SysCtrl.c file. 48 InitSysCtrl(J); 48</pre>						<b>=</b> -
49 50// Step 2. Initialize GPIO: 51// This example function is found in the F2837xD_Gpio.c file and 52// illustrates how to set the GPIO to it's default state. 53 InitGpio(); 54// Step 3. Clear all interrupts and initialize PIE vector table: 55// Disable CPU interrupts 56 DINT; 57 57 58// Initialize the PIE control registers to their default state. 4						+
📮 Console 🖾						⊒ - 📬 - 🗆
adc_soc_software_cpu01						
C28xx_CPU1: If erase/program (E/P) operation is being done on one core, th the E/P code. Also, CPU1 will be halted to determine SR ownership for the run its application. User code execution from SR could commence after both C28xx_CPU1: GEL Output: Memory Map Initialization Complete	e other cord CPU which w flash bank	e should not ex will run the Fl s are programme	ecute from s ash Plugin c d.	hared-RAM (S ode, after w	R) as they an hich CPU1 wil	e used for A l be set to
□° 🗟 Licensed						



常用菜单:

View 菜单: 可调出反汇编窗口 Disassembly, 全局变量观察窗 Expression, 存储观察窗 Memory Browser



Run 菜单: Load 子菜单可以用来加载.out 文件

Ī	Run	Scripts Window Help							
	-	Connect Target	Ctrl+Alt+C	1			😭 🎭 CCS Debug	📑 CCS Edit	
		Disconnect Target	Ctrl+Alt+D	able		1919 Pegisters		-	
]	疱	Restore Debug State	Alt+E	able		nor registers	<u> </u>	-4   4 -	- N-
1	₽	Load	•	0	Load Program			Ctrl+Alt+L	2
ł		Resume	F8	1	Reload Program			Ctrl+Alt+R	F
		Suspend	Alt+F8	للله	Load Symbols				Ŀ
		Terminate	Ctrl+F2	\$	Add Symbols				Ŀ
1	14	Disconnect		Ø	Verify Program				E
)	•	Go Main	Alt+M	*	Remove All Symbols				Ŀ
	٢	Reset	•	Ø	C:\ti\device_support	\CPU1_RAM\ad	dc_soc_software_cpu01.out		
	D.		L	_					

11. 保存 memory 中的测试结果

点击 Memory Browser 中的 save memory 🧖

ſ	Memory Bro	owser 🛙														(	) 🖉		•	<b>()</b>	) [	1 🗹 โ	~ - 6	3
	Data 🔻	0x012000														$\sim$	Save N	/lemo	ry I	•	Go	New	Tab	
l	Data:0x12000 -	0x012000 <	Memory	Rende	ering 1	> 23													_					
	16-Bit Hex - TI	Style	-																					
	0x00012000	Filter:	L Resul	t																			-	
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I	0x00012016	1111 11	11 1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111		
I	0x0001202C	1111 11	11 1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111		
I	0x00012042	1111 11	11 1111	11111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111		
I	0x00012058	1111 11	11 1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111		
I	0x0001206E	1111 11	11 1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111		a
I	0x00012084	1111 11	11 1111	11111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	E	
I	0x0001209A	1111 11	11 1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111		1
I	0x000120B0	1111 11	11 1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	_	1
I	0x000120C6	1111 11	11 1111	11111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111		
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I	0x000120F2	1111 11	11 1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111		
1	0x00012108	1111 11	11 1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111	1111		
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File 中选择该路径, dat 文件格式, 点击 next



选择数据格式,起始地址,存储长度,如下图,Finish。

Save Memory
Save Memory
Enter the information for the memory block to be saved
Format: 16-Bit Signed Int
Target
Start Address: 0x012000
Memory Page: Data
Length:
Specify the number of memory words to read:     32768
Specify the data block dimension in number of memory words:
Number of Rows: SNumber of Columns:
<         Back         Next >         Finish         Cancel

## 12. 另一种烧写程序的方式:

1) 右键目标配置文件: Launch Selected Configuration, 自动进入调试界面



2) 连接目标芯片:选择 CPU1,点击 connect device 按钮

CCS Debug - sdfm_filters_sync_cpu_cpu01/sdfm_filters_sync_cpu_cpu01.c - Code Composer Studio							
<u>File</u> Edit <u>View</u> <u>Project</u> <u>T</u> ools <u>R</u> un Scripts <u>W</u> indow	<u>H</u> elp						
🔁 🕶 🔚 🕼 😂 🕸 🕶 🌘 🖓 🕲	<i>*</i>						
🏇 Debug 🛛	🎉 🕪 III 🔳 (b. co. co. co. le) 🤹 🗸 😓 🧔 🏹 🗖						
F2837x XDS100.ccxml [Code Composer Studio - Device Debugging]							
Texas Instruments XDS100v2 USB Emulator_0/C28xx_CPU1 (Disconnected : Unknown)							
Texas Instruments XDS100v2 USB Emulator_0/CPU1_CLA1 (Disconnected : Unknown)							
Texas Instruments XDS100v2 USB Emulator_0/C28xx_CPU2 (Disconnected : Unknown)							
Texas Instruments XDS100v2 USB Emulator_0/CPU2_CLA1 (Disconnected : Unknown)							

3) 烧写(加载)程序: run > load > load program,浏览.out 文件,点击 ok 即可。

CC3 Debug - Source not it	Junu Cour	composer studio				A COMPANY AND INCOME AND A COMPANY AND A	
File Edit View Project	Tools Run	cripts Window H	elp				
	\$ • I	Connect Target Disconnect Target	Ctrl+Alt+C Ctrl+Alt+D			Ma Varishlar (70° Evoracione 52) 1999 Desictor	
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▲ ₩ F283/x_XDS100.ccxm	VDC10	Load	í í k	(A)	Load Program	Ctrl+Alt+	L
<ul> <li>a math by the set of the set o</li></ul>	s XDS10 s symbo s XDS10 s XDS10 s XDS10 s XDS10	Resume Suspend Terminate Disconnect Go Main	F8 Alt+F8 Ctrl+F2 Alt+M	× 19 * 10 ×	Reload Program Load Symbols Add Symbols Verify Program Remove All Symbols	Ctrl+Alt+	R
😵 Load Progra Program file	m _cpu01	\\ccs\CPU1_R/	AM\sdfm_filters_sync_	cpu	ı_cpu01.out	Browse) Browse project)	
Code offset							
Data offset							
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